

CLAIMS

What is claimed is:

1. A method for providing remote procedure calls in a multiprocessing system, the multiprocessing system including a general purpose processor and a plurality of network processors; each of the plurality of network processors having a memory, the method comprising the steps of:

- (a) accessing a reserved address in at least one of the network processors;
- (b) initiating a software action by a first portion of the reserved address; and
- (c) pointing to an address within the memory of the at least one network processor to be processed based upon data in a second portion of the reserved address; wherein the data at the address is processed.

2. The method of claim 1 wherein the reserved address comprises one instruction.

3. The method of claim 1 wherein each of the network processors include a reserved address.

4. The method of claim 1 wherein a location of the reserved address of each network processor is known by the other processors.

5. The method of claim 4 wherein the reserved addresses of each network processor is in the same location of memory.

1 6. A system for providing remote procedure calls in a multiprocessing system, the
2 multiprocessing system including a general purpose processor and a plurality of network
3 processors; each of the plurality of network processors having a memory, the system
4 comprising:
5 means for accessing a reserved address in at least one of the network
6 processors;
7 means for initiating a software action by a first portion of the reserved address;
8 and
9 means for pointing to an address within the memory of the at least one network
10 processor to be processed based upon data in a second portion of the reserved address; wherein
11 the data at the address is processed.

1 7. The system of claim 6 wherein the reserved address comprises one instruction.

1 8. The system of claim 6 wherein each of the network processors include a
2 reserved address.

1 9. The system of claim 6 wherein a location of the reserved address of each
2 network processor is known by the other processors.

1 10. The system of claim 9 wherein the reserved addresses of each network
2 processor is in the same location of memory.